Opportunità di stage





PLM (Product Lifecycle Management) & IP design management Integration

Product LifeCycle Management (PLM) is an initiative based on a global digital platform that manages and links processes, all product & technology intellectual and physical assets. It integrates people, processes, information and business systems, from customer/market need to product disposal. PLM represents a new collaborative way of working.

IPs are taking a central role in the semiconductor design process; it is a good idea to manage the entire design as a collection of IPs. IP design management is mandatory for security, confidentiality, IP reusability, business reputation and profitability.



We want implement a stable, secure and user-friendly application to integrate and unify IP design with PLM.

Another aim is to evaluate and choose the data management system that fits these needs to implement the best methodology for AMS group.



We are looking for a *stage* student willing to create this powerful application. The candidate will permit to the employee to manage ST products in a new way (from PLM and IP design management). The application should be a GUI implementing all basic IP management and PLM commands.

Recommended (but not mandatory) a basic knowledge of Data Management Tools and a web-oriented or object-oriented programming language.

It is required at least 6 months effort.

Stage student will receive monthly allowance, daily complimentary access to company restaurant and free shuttle.

Manda il tuo Curriculum a: <u>giuseppe-hr.finocchiaro@st.com</u> <u>ignazio.blanco@st.com</u>