

Davide Patti is currently an Assistant Professor at the Electric, Electronics, and Computer Engineering Department of the University of Catania (DIEEI). He has published several works on Multi-objective Design Space Exploration, Low-power techniques for Embedded Systems, and Network-on-Chip architectures. Davide Patti has been teaching several courses in the Computer Science field, holding courses in Human-Computer Interaction and Artificial Intelligence at the Faculty of Psychology of Kore University (Enna, Italy) and lectures at the University of Southampton (UK). He is a member of the Steering Committee of the International Workshop on Network on Chip Architectures (NoCArc) held in conjunction Annual IEEE/ACM International Symposium on Microarchitecture and has served as General Chair of the same workshop in 2017. He is also co-author of the Noxim project, currently used by a vast community of researchers working in the area of on-chip communication systems and employed as an experimental platform in more than 400 scientific papers published in refereed ACM and IEEE international conferences and journals.

Organization/Editorial Services

Organization

- **Special Session Chair and Organizer:** "Emerging Technologies for Reconfigurable Systems in the Manycore Era.", IEEE 11th International Symposium on Reconfigurable Communication-centric Systems-on-Chip (ReCoSoc), June 27-29, 2016, Tallinn - ESTONIA
- **TPC Chair:** ACM 10th International workshop on Network on Chip Architectures (NoCArc), tenuto all'interno di 49th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO-49), Taipei, Taiwan
- **Session Chair and Organizer:** Energy Efficient Cyber Physical Systems, The IEEE 30th International Conference on Microelectronics (ICM2018), Sousse, Tunisia (dal 16/12/2018 al 19/12/2018)
- **Invited Speaker:** Approximate on-chip communication, 14th HiPEAC Conference on High Performance Embedded Architectures and Compilers (HiPEAC), January 21st in Valencia, Spain
- **General Chair:** ACM 10th International workshop on Network on Chip Architectures (NoCArc) tenuto all'interno di 50th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO-50), Boston, USA
- **Special Session Chair and Organizer:** ACM/IEEE International Conference on Hardware/Software Codesign and System Synthesis (CODES+ISSS 2017), Special Session on Approximate Computing for Scalable and Energy Efficient Embedded Systems, Seoul, South Korea

TPC member

- Membro TPC: Digital Design & Architecture track in 33rd edition of VLSI Design conference, Bangalore, Jan 2020.

- Membro TCP: 10th International green and sustainable computing conference (IGSC 2019) - IEEE Computer Society & STC Sustainable Computing
- Membro TCP: 19th IEEE International Conference on Scalable Computing and Communications (ScalCom-2019)
- Membro TPC: 31st IEEE International System-on-Chip Conference (SOCC) 2019, Singapore
- Membro TPC: International Conference on Future Computational Technologies and Applications (FUTURE COMPUTING) (dal 01/01/2018 ad oggi)
- Membro TPC: ACM International Workshop on Network on Chip Architectures (No-CArc) held in conjunction with the Annual IEEE/ACM International Symposium on Microarchitecture (MICRO) (edizioni dal 12/12/2009 al 05/12/2015)
- Membro TPC: 4th ACM International Workshop on Manycore Embedded Systems, Jun 18, 2015 - Jun 19, 2015, Seoul, South Korea
- Membro TPC: International Conference on Pervasive and Embedded Computing (PEC) (dal 01/12/2015 ad oggi)
- Membro TPC: 18th IEEE International Conference on Scalable Computing and Communications (ScalCom 2018) , GuangZhou.
- Membro TPC: International Conference on Signal Image Technology & Internet Based Systems (SITIS) (dal 01/01/2017 ad oggi)

Editorial Services

- **Topic Editor:** MDPI Electronics (ISSN 2079-9292, Scopus id 21100829272)
- **Editorial Board:** Journal of Sensor and Actuator Networks , MDPI, (ISSN 2224-2708)
- **Lead Guest Editor:** Special Issue on Internet of Things Technologies for Low Power Cyberphysical Systems, in VLSI Design Journal, Hindawi Publishing Corporation (Scopus id 62723)
- **Guest Editor:** Special Issue "Emerging Network-on-Chip Architectures for Low Power Embedded Systems", in Journal of Low Power Electronics and Applications, MDPI (Scopus id 21100332242)
- **Guest Editor:** Special Issue "Cyber-Physical Systems: Data Processing and Communication Architectures" in MDPI Technologies (Web of Science Indexed, Quarterly)
- **Guest Editor:** Special Issue "Low-power Techniques for Embedded Systems and Network-on-Chip Architectures" in Electronics (ISSN 2079-9292, Scopus id 21100829272)

Publications

<https://scholar.google.com/citations?user=T1QggR0AAAAJ&hl=it>